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Five Years Out

Arrow Electronics & Analog Devices RF Seminar
"Ride the wave of innovation with the industry's broadest RF portfolio"

Date:	24, 25, 26 September	**Duration:**	Full Day
Start:	08:30	**Language:**	Italian / English
Place:	Milan, Florence, Rome	**End:**	16:00

Arrow Electronics and **Analog Devices** are pleased to propose you a seminar on RF Technologies and Solutions. Over the entire RF spectrum, from DC to beyond 100GHz, ADI can offer the deepest expertise and the widest array of technologies, including CMOS, SiGe, BICMOS, SOI, GaAs and GaN, in discrete or integrated solutions, for Communications, Test & Measurement Instrumentation, Industrial and Aerospace & Defence markets.

Location:

24 September	**Milan**	Excelsior Hotel Gallia – Piazza Duca D'Aosta 9
25 September	**Florence**	Hilton Garden Inn Florence Novoli - Via Sandro Pertini 2/9
26 September	**Rome**	Hotel The Building - Via Montebello 126

Registration:
https://connection.arrow.com/arrow_emea_en_ADI_RF_Seminar_sep2019

	AGENDA
08:30	Registration and welcome coffee
09:00	Welcome (Arrow Electronics & Analog Devices)
09:10	**Analog Devices RF / uW Solutions** **(Salvatore Napolitano – Analog Devices Field Application Engineer):** •Products update •Functional Integration •RF Converters
10:00	**Deep Dive into Analog Devices Wireless Sensor Networks Solutions and Technologies** **(Paul Hartanto – Doeser, Analog Devices WSN Subject Matter Expert):** •WSN Introduction: main challenges (Reliability,latency, low power, etc) and network topologies •Time Slotted Channel Hopping: how TSCH meet challenges in WSN •ADI WSN Solutions: HW and Protocol for SmartMesh IP – AgileNet IP – RapidNet IP
11:15	Coffee Break
11:30	**Deep Dive into Analog Devices SDR (Software Defined Radio) Solutions and Technologies** **(Danish Aziz – Analog Devices RF Products Subject Matter Expert):** •Value and Applications of the SDR •Architectures and Transceivers: Signal Path description, Performances and Trade-offs, Analog and Digital Gain Controls, Multi Chip Synchronization •System Design and Interfaces: SDR Development tools, Matlab etc., Reference Designs, JESD Interface •Front End Designs for SDRs - e.g. LNAs and PAs •SDR SOMs and Roadmaps
13:00	Lunch
14:00	**A real life RF/uW Project, with technologies and design insights (Analog Devices Partner)**
16:00	Conclusion